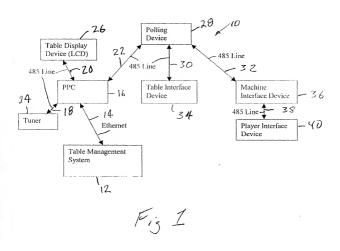
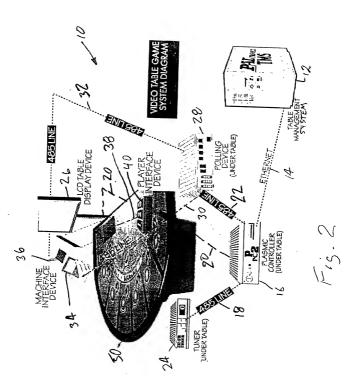
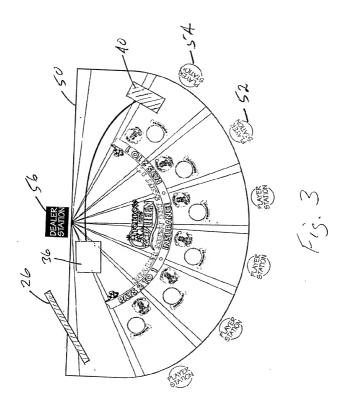
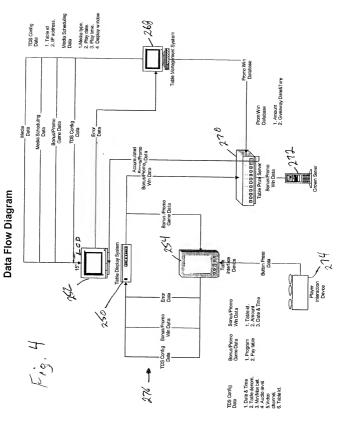
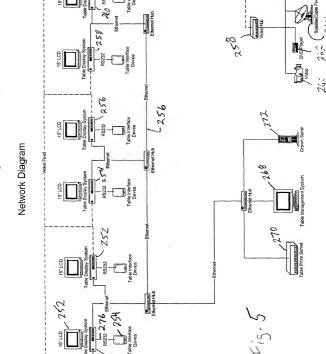
System Diagram

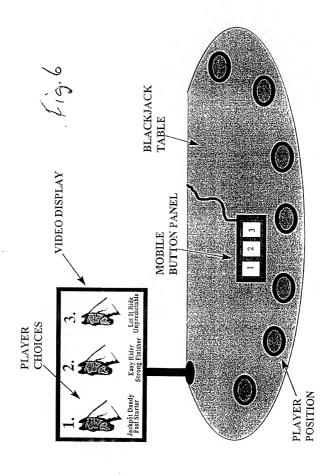


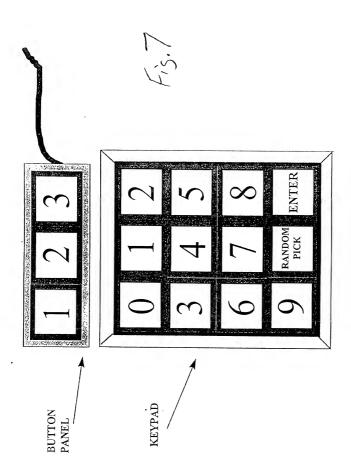


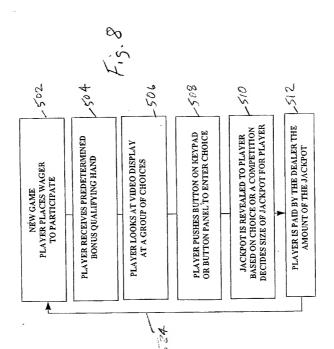


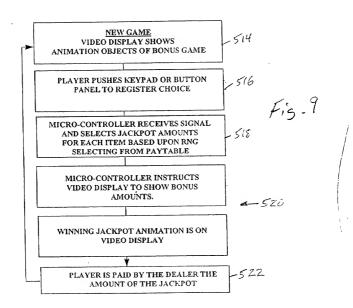




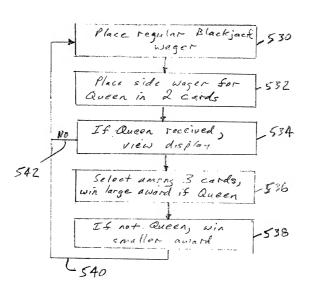


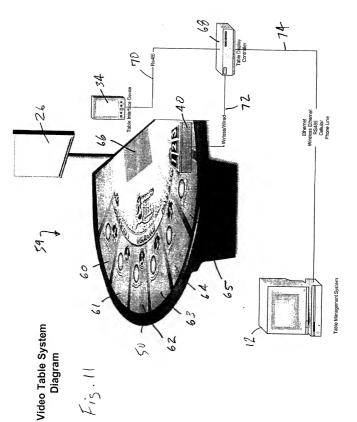






Tis. 10





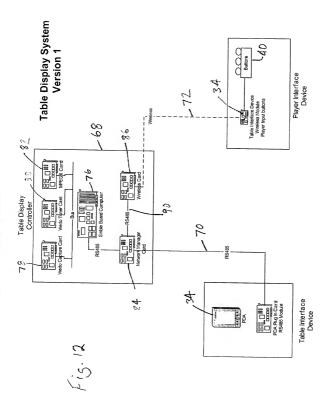
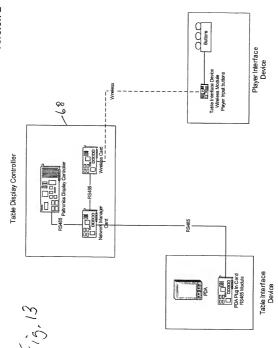
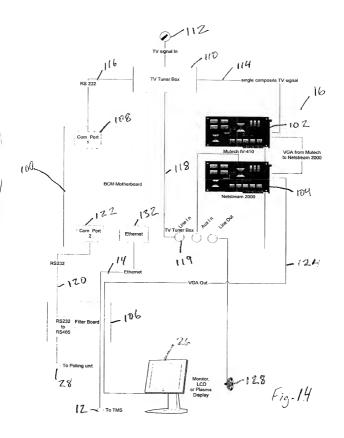


Table Display System Version 2





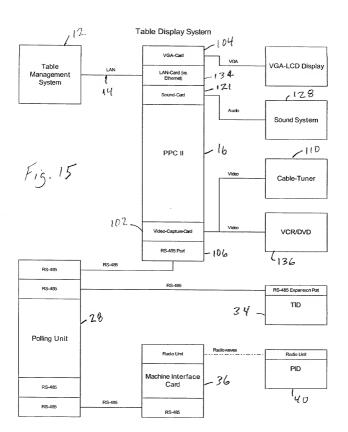


Table Input Device General Initialization

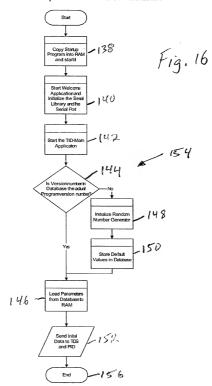


Table Input Device User Interface

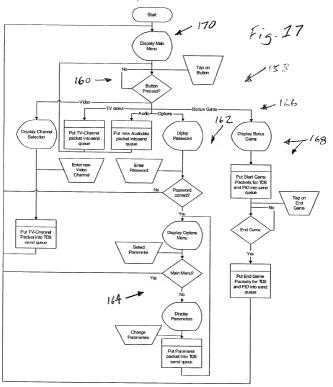
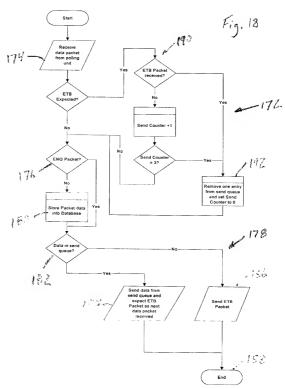
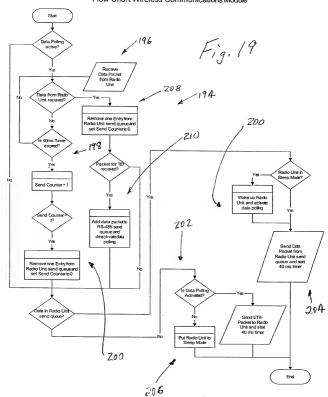
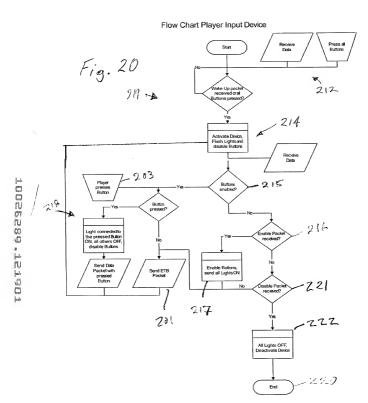


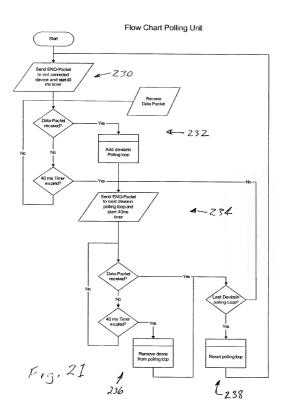
Table Input Device Serial Communication

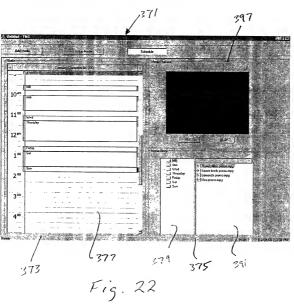


Flow Chart Wireless Communications Module









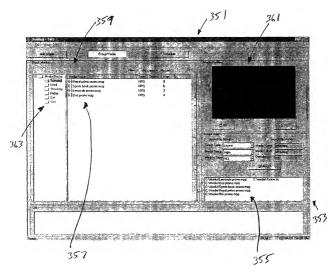


Fig. 23

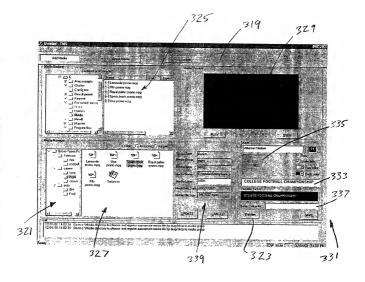


Fig. 24

Fig. 25 A







